

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made.

France

7,247,216
7,345,377

J. E. Earlet and F. Bigo
H. Nussbaumer

Improvements for Data Communication Systems
Improvements for Digital Filters

Germany

2,264,752

A. Blum, C. Mohr, H. R. Geng,
F. Jrrro, L. Reichl and G. Sonntag

Data Transmission Between Units of a Data Processing System

United Kingdom

1,487,507
1,487,945
1,488,038
1,488,538

J. G. Axford and D. M. Youmans
W. A. Warwick
P. A. Gardner
P. A. Sheppard

Information Retrieval System
Improvements Relating to Semiconductor Integrated
Data Storage Apparatus
Compressed Refresh Buffer

United States

4,065,810

C. E. Cramer, J. R. Gaetjen,
C. H. Grant, P. E. Nelson and
F. A. Newlin III

Data Transfer System

4,066,312

L. H. Faure

High Density Cable Connector

4,066,458

P. E. Stuckert

Mask-Exposure Scheme

4,066,491

W.-D. Ruh and G. Trippel

Method of Simultaneously Etching Multiple Tapered Viaducts in
Semiconductor Material

4,066,809

W. A. Alpaugh, G. J. Macur and
G. P. Vlasak

Method for Preparing Substrate Surfaces for Electroless Deposition

4,066,863

S. K. Doran and M. H. Perkins

Method and System for Automatically Correcting Aberrations of a Beam of
Charged Particles

4,066,883

D. R. Wheeler, Jr.

Test Vehicle for Selectively Inserting Diagnostic Signals into a Bus-
Connected Data-Processing System

4,067,002

H. Chang

Field Access of Bubble Domain Lattice

4,067,019

J. M. Fleischer and R. D. Holmes

Impact Position Transducer for Ink Jet

4,067,649

J. H. Hubbard, R. J. LeClere and
T. T. Underhill

Method and Apparatus for Controlling the Duplex Copy Mode in an
Electrostatic Copying Device

4,068,019

R. S. Boeckl

Spin Coating Process for Prevention of Edge Buildup

4,068,174

L. E. Svelund

Digital Carrier Wave Detect Circuitry

4,068,217

P. C. Arnett and J. J. Chang

Ultimate Density Non-Volatile Cross-Point Semiconductor Memory Array

4,068,220

B. E. Argyle, P. Dekker and
J. C. Slonczewski

Controllable State Conversions For S=1 Bubble Domains

4,068,224

B. Bechtel, H. J. Bleher,
H. Hasselmeier, W. G. Spruth,
P. Stucki and H. Weis

Compressed Information Storage for Characters Consisting of Black and
White Areas Which are to be Displayed or Printed

4,068,226

R. A. Bowman and M. M. Burd

Apparatus and Method for Data Entry and Display

4,068,240

G. J. Fan, D. C.-W. Lo and
J. W. Mitchell

Vector Magnetic Ink Jet Printer with Stabilized Jet Stream

4,068,269

R. D. Commander and J. R. Taylor

Positioning System for Data Storage Apparatus and Record Medium for
Use Therewith

4,068,270

C. D. Hurd and R. P. Loomis

Device to Support and Position a Magnetic Transducer

4,068,304

W. F. Beausoleil and K. Maling

Storage Hierarchy Performance Monitor

4,068,373

T. L. Bell, L. G. Call, G. O. Dillan,
J. P. Jackson, Jr. and L. A. Johnson

Component Insertion Machine

4,068,837

R. A. Lamos

Paper Hold-Down Device for Collector

4,068,839

M. K. Bullock, S. A. Clay and
G. B. Overton

Sheet Stacking Apparatus

4,068,994	R. H. Cadwallader, Y. Darves-Bornoz, A. S. Gasparri and F. A. Racine	Apparatus for the Printing of Ceramic Green Sheets
4,069,068	K. D. Beyer, G. Das, M. R. Poponiak and T.-H. Yeh	Semiconductor Fabrication Method for Improved Device Yield by Minimizing Pipes Between Common Conductivity Type Regions
4,069,428	D. C. Reedy	Transistor-Transistor-Logic Circuit
4,069,463	J. C. McGroddy and P. S. Zory, Jr.	Injection Laser Array
4,069,485	V. C. Martin	Bidirectional Ink Jet Printer with Moving Record Receiver
4,069,486	S. J. Fox	Single Array Ink Jet Printer
4,069,488	R. M. Fiorenza, A. J. Fleming, R. J. Gerlach, L. W. Holmstrom and J. C. Pace	Computer Controlled Distribution Apparatus for Distributing Transactions to and from Controlled Machines Tools
4,069,498	K. C. Joshi	Studded Heat Exchanger for Integrated Circuit Package
4,069,957	J. S. Moffitt	Burster Method
4,070,116	A. Frosch, W. Mannsdorfer and C. Scheuing	Gap Measuring Device for Defining the Distance Between Two or More Surfaces
4,070,148	F. J. Campagna and G. F. Pavlovic	Apparatus for Monitoring Product Temperature in an Open Ended, Secondary Emission, Product Carrying Conveyor Furnace
4,070,264	M. Loiseau and P. C. Pileur	R.F. Sputtering Method and Apparatus
4,070,501	V. R. Corbin, J. E. Hitchner, B. Patnaik and C.-Y. Ting	Forming Self-Aligned via Holes in Thin Film Inter-Connection Systems
4,070,516	H. D. Kaiser	Multilayer Module Having Optical Channels Therein
4,070,656	K. Heuber, W. Klein, K. Najmann and S. K. Wiedmann	Read/Write Speed Up Circuit for Integrated Data Memories
4,070,658	E. A. Giess, G. E. Keefe and Y. S. Lin	Ion Implanted Bubble Propagation Structure
4,070,679	G. J. Fan and J. C. Slonczewski	Method and Apparatus for Recording Information on a Recording Surface by the Use of Magnetic Ink
4,070,687	I. T. Ho and J. Riseman	Composite Channel Field Effect Transistor and Method of Fabrication
4,071,287	T. A. Sherk	Manufacturing Process for Gaseous Discharge Device
4,071,783	R. W. Knepper	Enhancement/Depletion Mode Field Effect Transistor Driver
4,071,869	L. B. Feierabend and D. S. Proper	Assembling Flexible Disk Record Structures
4,071,902	E. B. Eichelberger and T. W. Williams	Reduced Overhead for Clock Testing in a Level System Scan Design (LSSD) System
4,071,903	S. M. Head and C. M. Melas	Autocorrelation Function Factor Generating Method and Circuitry Therefor
4,072,101	J. La Garcia, B. M. Griffing, P. Y.-F. Hu and R. G. Millington	Linear Actuator Printer Carriage
4,072,225	D. F. Manning and R. A. Ryan	Combined Pin Feed and Pressure Roll Forms Advancement Apparatus
4,072,545	F. H. De La Moneda	Raised Source and Drain IGFET Device Fabrication
4,072,816	R. W. Gedney and J. Rasile	Integrated Circuit Package
4,072,864	R. J. von Gutfeld	Multilayered Slant-Angle Thin Film Energy Detector
4,072,868	F. H. De La Moneda and H. N. Kotecha	FET Inverter with Isolated Substrate Load
4,072,888	B. Bechtle, C. Schuenemann, G. Skudelny, W. Spruth, H. Weis and V. Zimmermann	Method of Controlling a Stepper Motor
4,072,939	L. G. Heller and L. M. Terman	Analog-to-Digital and Digital-to-Analog Converter Circuits Employing Charge Redistribution
4,072,990	W. J. P. Case and R. D. Commander	Servo Positioning System for Data Storage Apparatus
4,074,016	G. Trippel	Magnetic Record Carrier
4,074,031	D. E. Johnson and L. A. Pederson	Process for Preparing Electron Beam Resists
4,074,066	W. F. Ehrsam, C. H. W. Meyer, J. L. Smith and W. L. Tuchman	Message Verification and Transmission Error Detection by Block Chaining
4,074,134	H. J. Roalson	Operating Point Regulation for a Corona Discharge Device
4,074,150	F. Buckley III, M. K. Creamer, Jr. and G. A. Miller	MOS Interchip Receiver Differential Amplifiers Employing Resistor Shunt CMOS Amplifiers
4,074,151	F. Buckley III, G. A. Miller and V. A. Scotto	MOS Interchip Receiver Differential Amplifiers Employing CMOS Amplifiers Having Parallel Connected CMOS Transistors as Feedback Shunt Impedance Paths
4,074,237	D. P. Spampinato	Word Line Clamping Circuit and Decoder
4,074,240	R. L. Melcher and N. S. Shiren	Magnetoelastic Storage Device
4,074,254	K. A. Belser and P. B. Metz	XY Addressable and Updateable Compressed Video Refresh Buffer for Digital TV Display
4,074,277	R. Lane and H. H. Taub	Apparatus for Acoustically Synchronizing Drop Formation in an Ink Jet Array
4,074,330	D. G. Norton and G. T. Wright	Flexible Disk Storage Apparatus
4,074,342	J. J. Honn and K. P. Stuby	Electrical Package for LSI Devices and Assembly Process Therefor
4,074,851	E. B. Eichelberger, E. I. Muehldorf, R. G. Walther and T. W. Williams	Method of Level Sensitive Testing a Functional Logic System with Embedded Array

4,075,045	V. L. Rideout	Method for Fabricating FET One-Device Memory Cells with Two Layers of Polycrystalline Silicon and Fabrication of Integrated Circuits Containing Arrays of the Memory Cells Charge Storage Capacitors Utilizing Five Basic Pattern Deliberating Steps
4,075,055	T. F. Ciszek and G. H. Schwuttke	Method and Apparatus for Forming an Elongated Silicon Crystalline Body Using a {110}{211} Orientated Seed Crystal
4,075,577	E. L. Walker	Analog-to-Digital Conversion Apparatus
4,075,582	E. L. Walker	Surface Acoustic Wave Device
4,075,636	L. V. Galetto, J. H. Meier, W. T. Pimbley and B. A. Wolfe	Bi-Directional Dot Matrix Printer with Slant Control
4,075,671	S. D. Cheatham and J. Lewkowicz	Automatic AC Biasing of a Magnetoresistive Element
4,075,693	J. L. Fox and E. E. Marquardt	Configuration and Control Unit for a Heterogeneous Multi-System
4,075,756	C. J. Kircher and H. H. Zappe	Process for Fabricating Above and Below Ground Plane Wiring on One Side of a Supporting Substrate and the Resulting Circuit Configuration
4,076,046	G. W. Hieronymus, M. L. Sendelweck, J. E. West and J. W. Woods	Fast Acting Two-Way Valve
4,076,357	A. B. Cistola	Laminated Programmable Microstrip Interconnector
4,076,372	R. V. Pole and H. P. Wollenmann	Two-Dimensional Light Deflector Apparatus
4,076,558	H. S. Rupprecht and R. O. Schwenker	Method of High Current Ion Implantation and Charge Reduction by Simultaneous Kerf Implant
4,076,575	K. Chang	Integrated Fabrication Method of Forming Connectors Through Insulative Layers
4,076,599	J. J. Caricchio, Jr. and E. R. York	Method and Composition for Plating Palladium
4,076,986	A. Croisier and R. Glaise	Device for Reproducing the Charge Stored in an Input Capacitor in a Plurality of Output Capacitors
4,077,011	J. R. Mathis	Uncertain Interval Timer Using a Variable Length Shift Register
4,077,021	I. P. Csajka and G. Ungerboeck	Method and Arrangement for Coding Binary Signals and Modulating a Carrier Signal
4,077,035	Y. S. Yee	Two-Stage Weighted Capacitor Circuit for Analog-to-Digital and Digital-to-Analog Converters
4,077,040	F. Hendriks	Guard Jets in Multiple Nozzle Printing
4,077,059	V. A. Cordi and B. A. Edson	Multi-Processing System with a Hierarchical Memory Having Journaling and Copyback
4,077,060	R. E. Bodner and R. C. Kiscaden	Asymmetrical Multiprocessor System